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Gallagher et al.

Method, System, and Computer Program Product for Dynamically Selecting
Software Buffers for Aggregation According to Current System Characteristics

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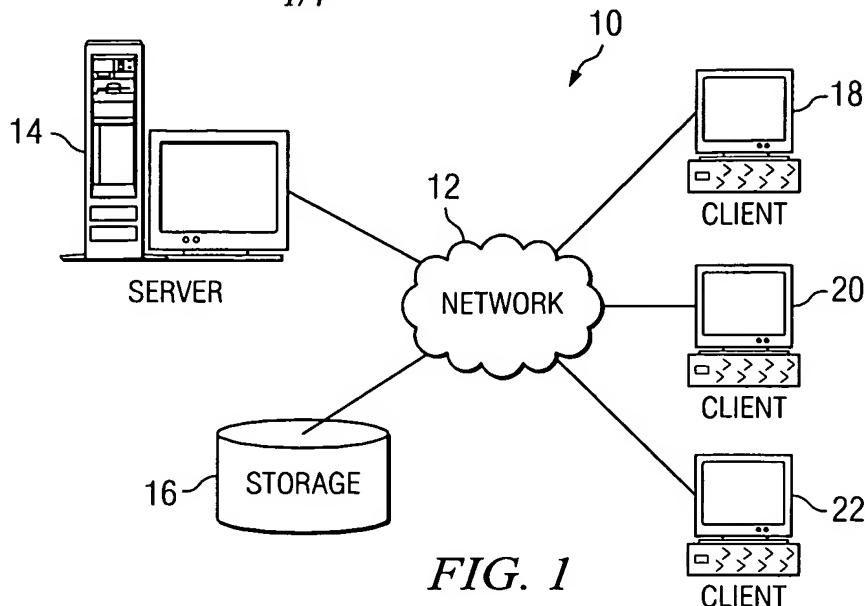


FIG. 1

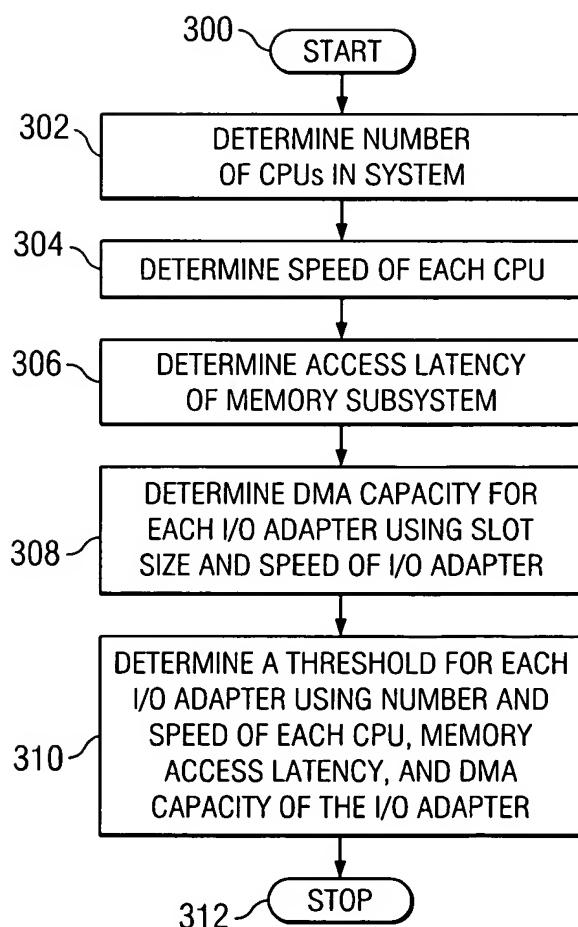


FIG. 3

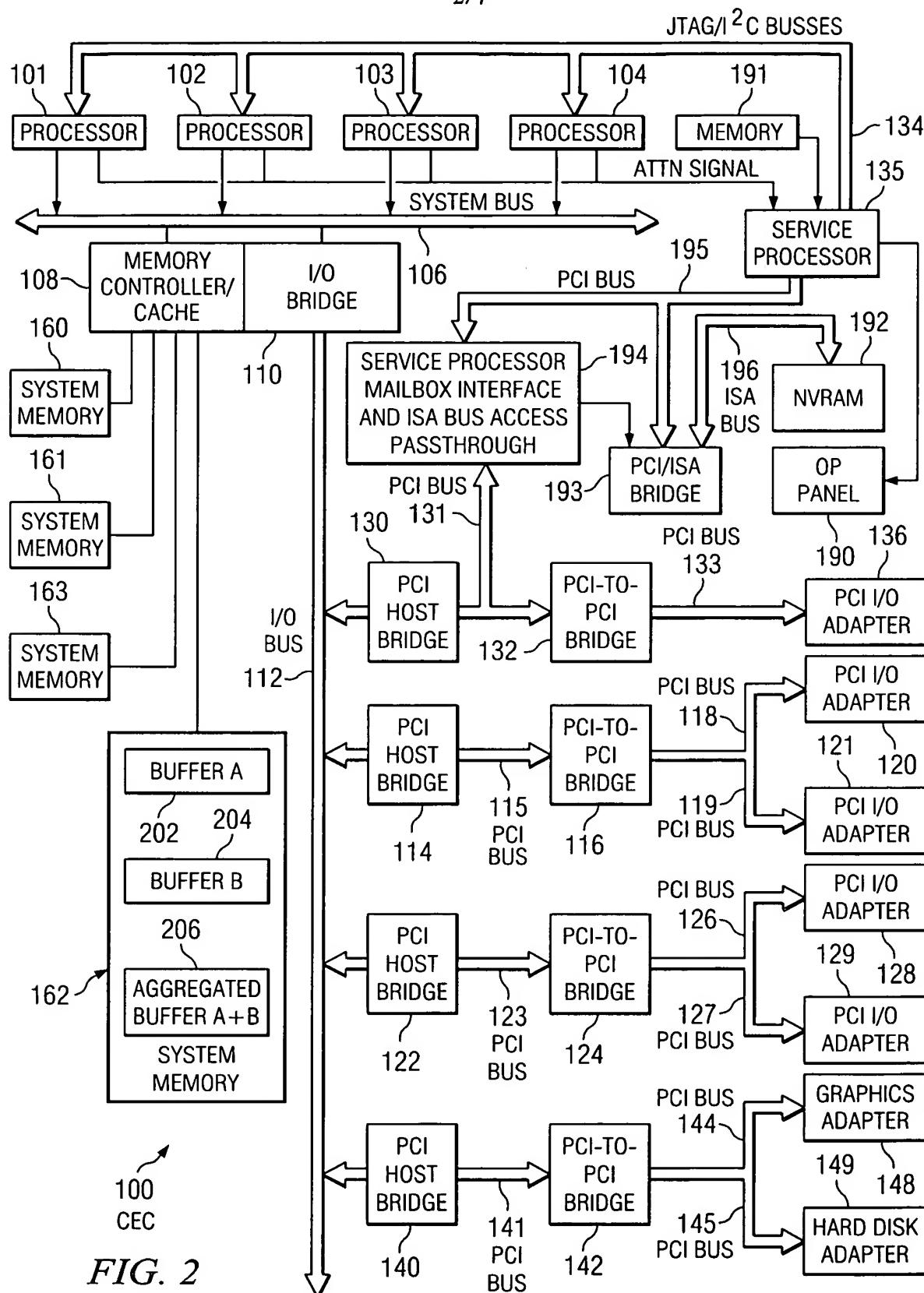


FIG. 2

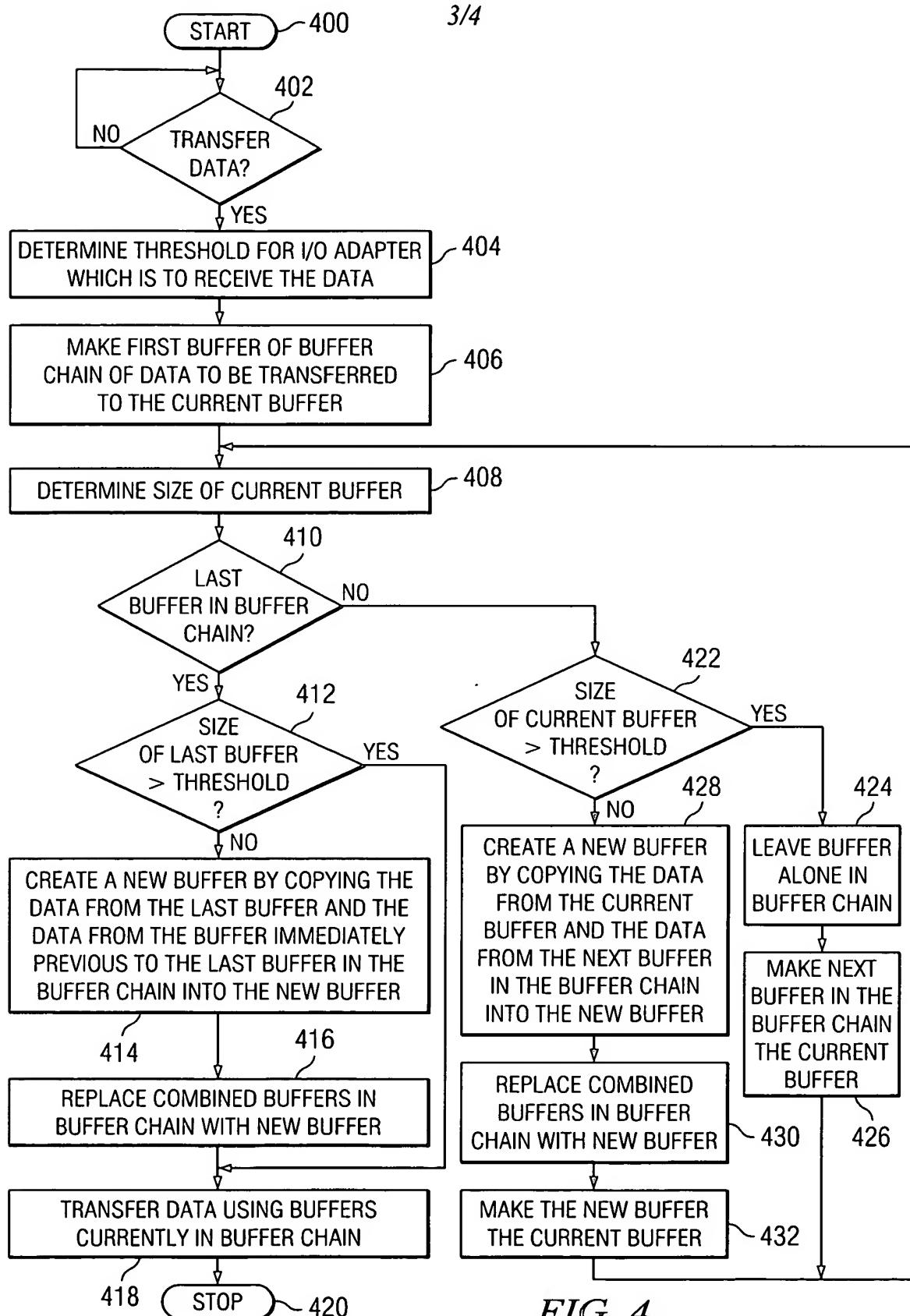


FIG. 4

CPU		MEMORY ACCESS LATENCY	DMA CAPABILITY		AGGREGATION THRESHOLD
NUMBER	SPEED		WIDTH	SPEED	
4	1.45 MHz	5 MICROSECONDS	32 BITS	33 MHz	2049 BYTES
				66 MHz	1792 BYTES
				133 MHz	1408 BYTES
				33 MHz	1792 BYTES
			64 BITS	66 MHz	1408 BYTES
				133MHz	1025 BYTES

FIG. 5

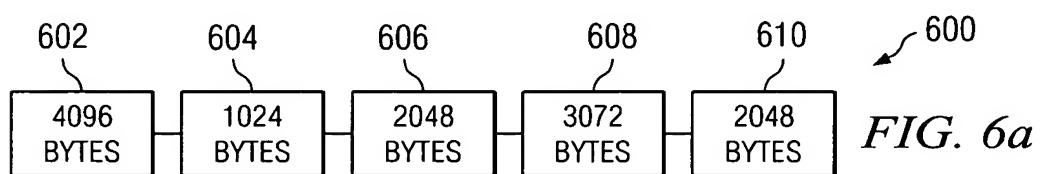


FIG. 6a

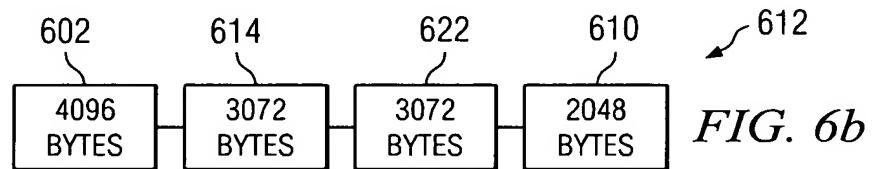


FIG. 6b

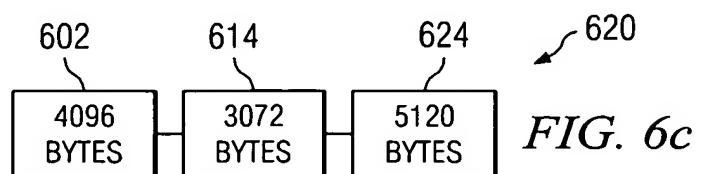


FIG. 6c